

FIG.2

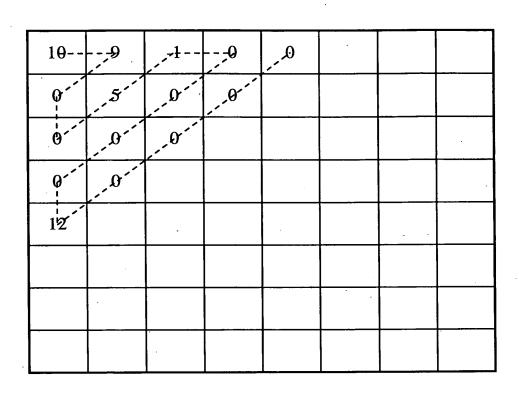


FIG.3

ZERO-RUN NUMBER	LEVEL VALUE	NUMBER OF (ZERO-RUN + LEVEL)'S
.0	10	4
3	5	
0	-1	
4	12	

	ER=0)	3ER=1)	ER=1) ER=2)			_	VLC LENGTH	VLC LENGTH	:		VLC LENGTH	VLC LENGTH	:		VLC LENGTH	VLC LENGTH	: :
DATA CONTENT	address0 (ZERO-RUN NUMBER=0)	address1 (ZERO-RUN NUMBER=1)	address2 (ZERO-RUN NUMBER=2)	•••	•••		VLC OF (Run, ILevell)=(0, 1)	VLC OF (Run, ILevell)=(0, 2)	•••	-	VLC OF(Run, Level)=(1, 1)	VLC OF(Run, Level)=(1, 2)	:		VLC OF (Run, Level)=(2, 1)	VLC OF (Run, ILevell)=(2, 2)	•••
ADDRESS	0000	1000	0002	:.	:	address0	address0+1	address0+2	:	address1	address1+1	address1+2	:	address2	address2+1	address2+2	

FIG.5

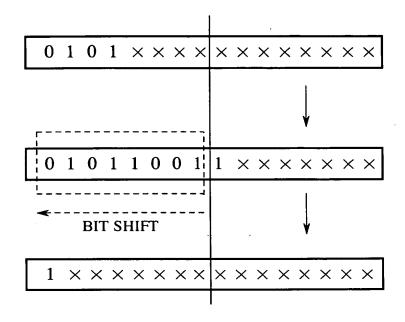


FIG.7

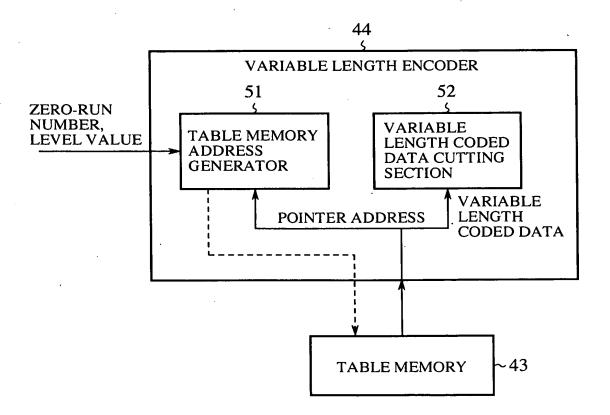


FIG.

_											•									
					VLC LENGTH	VLC LENGTH			VLC LENGTH	VLC LENGTH		1	VLC LENGTH	VLC LENGTH	:	n BITS				
DATA CONTENT	address0 (ZERO-RUN NUMBER=0)	address1 (ZERO-RUN NUMBER=1)	address2 (ZERO-RUN NUMBER=2)	••			VLC OF (Run, ILevell)=(0, 1)	VLC OF (Run, ILevell)=(0, 2)	•••		VLC OF (Run, Level)=(1, 1)	VLC OF (Run, ILevell)=(1, 2)	•		VLC OF (Run, ILevell)=(2, 1)	VLC OF (Run, Level)=(2, 2)	:	m BITS	L BITS (L=m+n)	
ADDRESS	0000	1000	0000	:	· :	address0	address0+1	address0+2	:	address1	address1+1	address1+2	:	address2	address2+1	address2+2	J ;	. · ·		7

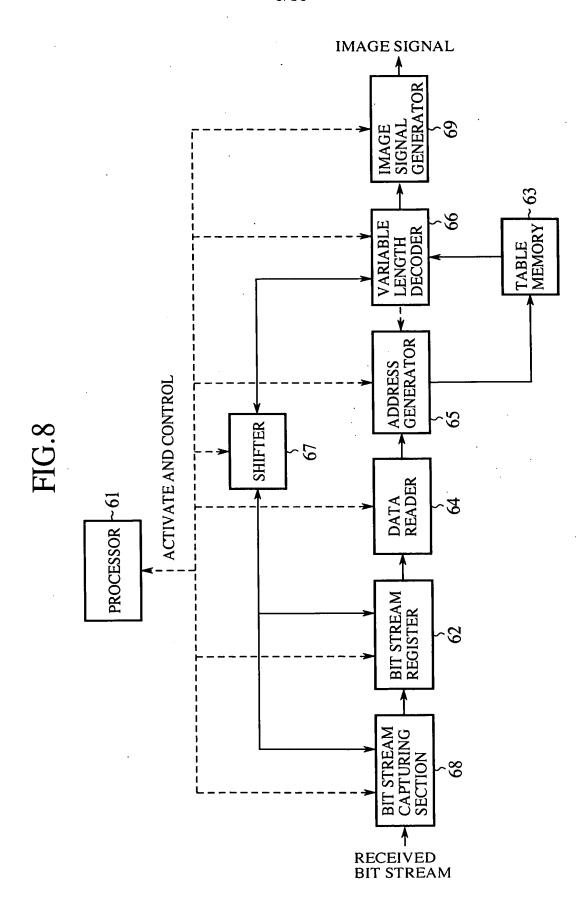


FIG.9

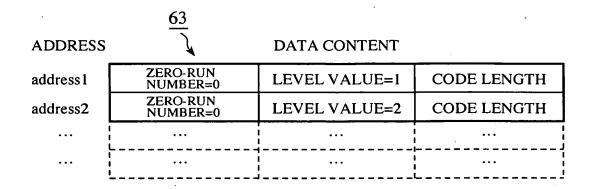


FIG.10

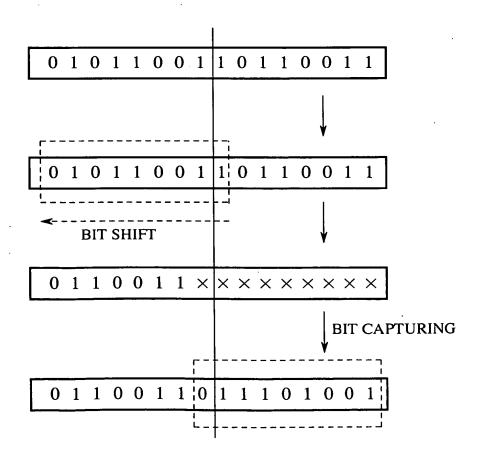


FIG.11A

ADDRESS		DATA CONTENT	
address1	ZERO-RUN NUMBER=0	LEVEL VALUE=1	CODE LENGTH
address2	ZERO-RUN NUMBER=0	LEVEL VALUE=2	CODE LENGTH
•••		•••	
•••			
		'	

CODING SCHEME A

FIG.11B

ADDRESS		DATA CONTENT	·
address1	ZERO-RUN NUMBER=0	LEVEL VALUE=1	CODE LENGTH
address2	ZERO-RUN NUMBER=0	LEVEL VALUE=2	CODE LENGTH
· •••	•••	•••	
•••			

CODING SCHEME B

FIG.12 (PRIOR ART) 7 1 **INFORMATION** HUFFMAN **MEMORY** TO TRANSMISSION LINE SOURCE **ENCODER** MULTIPLEXER 6 **HUFFMAN TABLE GENERATOR STOCHASTIC CALCULATION QUANTIZER** P(A), P(B)P(C) P(D) P(E) 5 **ARITHMETIC** ARITHMETIC **CIRCUIT CIRCUIT** P(SUM) P(SUM) P1(C)=P1(D)=P1(E)=P(C)+P(D)+P(E)=P(SUM)/3

FIG.13 (PRIOR ART)

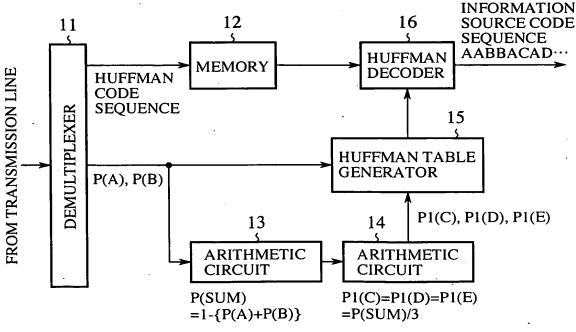


FIG.14 (PRIOR ART)

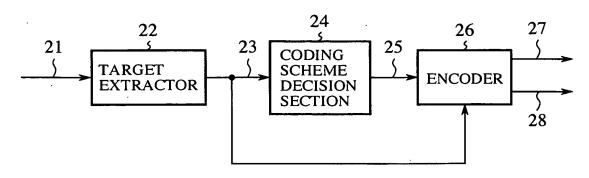


FIG.15 (PRIOR ART) <u>26</u> 35 31 -FIRST ENCODER 32 SECOND ENCODER ENCODER SELECTOR 27 33 25 د 28 THIRD ENCODER 34 nTH ENCODER **2**3